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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
09/900,224	07/06/2001	Jeffrey D. Carr	45188/FLC/B600	4002

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EXAMINER	
PARTHASARATHY, PRAMILA	
ART UNIT	**PAPER NUMBER**
2136	

DATE MAILED: 11/03/2005

Please find below and/or attached an Office communication concerning this application or proceeding.

Office Action Summary	Application No.	Applicant(s)	
	09/900,224	CARR, JEFFREY D.	
	Examiner	Art Unit	
	Pramila Parthasarathy	2136	

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) OR THIRTY (30) DAYS, WHICHEVER IS LONGER, FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

Status

1)☒ Responsive to communication(s) filed on *29 August 2005*.

2a)☐ This action is **FINAL**. 2b)☒ This action is non-final.

3)☐ Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

Disposition of Claims

4)☒ Claim(s) *1 - 16* is/are pending in the application.

4a) Of the above claim(s) *6* is/are withdrawn from consideration.

5)☐ Claim(s) _____ is/are allowed.

6)☒ Claim(s) *1-4,7-9 and 11-16* is/are rejected.

7)☒ Claim(s) *5 and 10* is/are objected to.

8)☐ Claim(s) _____ are subject to restriction and/or election requirement.

Application Papers

9)☐ The specification is objected to by the Examiner.

10)☐ The drawing(s) filed on _____ is/are: a)☐ accepted or b)☐ objected to by the Examiner.

Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).

Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).

11)☐ The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

Priority under 35 U.S.C. § 119

12)☐ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).

a)☐ All b)☐ Some * c)☐ None of:

1.☐ Certified copies of the priority documents have been received.

2.☐ Certified copies of the priority documents have been received in Application No. _____.

3.☐ Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).

* See the attached detailed Office action for a list of the certified copies not received.

Attachment(s)

1) ☒ Notice of References Cited (PTO-892)
2) ☐ Notice of Draftsperson's Patent Drawing Review (PTO-948)
3) ☐ Information Disclosure Statement(s) (PTO-1449 or PTO/SB/08) Paper No(s)/Mail Date _____.
4) ☐ Interview Summary (PTO-413) Paper No(s)/Mail Date. _____ .
5) ☐ Notice of Informal Patent Application (PTO-152)
6) ☐ Other: _____.

DETAILED ACTION

Continued Examination Under 37 CFR 1.114

1. A request for continued examination under 37 CFR 1.114, including the fee set forth in 37 CFR 1.17(e), was filed in this application after final rejection. Since this application is eligible for continued examination under 37 CFR 1.114, and the fee set forth in 37 CFR 1.17(e) has been timely paid, the finality of the previous Office action has been withdrawn pursuant to 37 CFR 1.114.

2. Applicant's submission filed on August 29, 2005 has been entered and made of record.

Claim Rejections - 35 USC § 112

3. Claims 1 – 16 are rejected under 35 U.S.C. 112, first paragraph, as failing to comply with the written description requirement. The claim(s) contains subject matter which was not described in the specification in such a way as to reasonably convey to one skilled in the relevant art that the inventor(s), at the time the application was filed, had possession of the claimed invention.

Amended Claims 1, 7 and new Claim 13 recite, "inversely hash", "inversely hashing" or "inversely hashed". Even though the instant specification discloses, "In another embodiment.... the parameter signal is transformed by using hashing function.", see instant application paragraph [0025] and "The data word signal... The second signal is then used to inversely transform the incoming...which is stored in a destination register 110.", see instant application paragraph [0045], the specification does not explicitly disclose "inversely hash", "inversely hashing" or "inversely hashed". Applicant remarks/amendment filed on 8/29/2005, does not clarify the steps of "inversely hash", "inversely hashing" or "inversely hashed".

The dependent claims 2 – 6, 8 – 12 and 14 – 16 are rejected at least by virtue of their dependency on the dependent claims.

Response to Arguments

4. Applicant's arguments with respect to claims 1 – 16 have been considered but are moot in view of the new ground(s) of rejection.

Claim Rejections - 35 USC § 102

The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless –

(e) the invention was described in (1) an application for patent, published under section 122(b), by another filed in the United States before the invention by the applicant for patent or (2) a patent granted on an application for patent by another filed in the United States before the invention by the applicant for patent, except that an international application filed under the treaty defined in section 351(a) shall have the effects for purposes of this subsection of an application filed in the United States only if the international application designated the United States and was published under Article 21(2) of such treaty in the English language.

5. Claims 1 – 4, 7 – 9, 11 – 16 are rejected under 35 U.S.C. 102(e) as being anticipated by Lotspiech et al. (U.S. Patent Number 6,118,873).

6. Regarding claim 1, Lotspiech teaches generating by the first device a control signal and a parameter signal (Summary and Column 5 lines 10 – 20);

encrypting or hashing by the first device a portion of the control signal with the parameter signal to generate an encrypted or hashed parameter signal and control signal (Summary and Column 5 line 55 – Column 6 line 8);

transmitting by the first device to the second device the control signal and the encrypted or hashed parameter signal and control signal (Summary; Column 5 lines 55 – 59 and Column 6 lines 8 – 32);

receiving by the second device from the first device the control signal and the encrypted or hashed parameter signal and control signal (Summary and Column 6 lines 3 – 8);

using by the second device the control signal to decrypt or inversely hash the encrypted or hashed parameter signal and control signal (Summary and Column 6 lines 8 – 12); and

generating by the second device a destination parameter signal depending upon a comparison of the control signal and the decrypted or inversely hashed control signal (Summary and Column 6 lines 29 – 50).

7. Regarding Claim 7, Lotspiech teaches a control logic block to receive a control signal comprising a key index and an encrypted or hashed signal that comprises an encrypted or hashed form of a parameter signal and a portion of the control signal;

an interface operation logic block operably coupled to the control logic block to decrypt or inversely hash the encrypted or hashed signal in accordance with the index to generate a destination parameter signal (Summary and Column 5 line 10 – Column 6 line 50).

8. Regarding Claim 13, Lotspiech teaches generating by the first device a control signal comprising a key index;

using, by the first device, at least a portion of the control signal to obtain a first cryptographic key;

encrypting or hashing using the first cryptographic key, by the first device, a first signal to generate an encrypted or hashed signal;

transmitting, by the first device to the second device, the control signal and the encrypted or hashed signal;

receiving by the second device from the first device the control signal and the encrypted or hashed signal;

using, by the second device, the key index from the control signal to obtain a second cryptographic key;

decrypting or inversely hashing using the second cryptographic key, by the second device the encrypted or hashed signal to provide a decrypted or inversely hashed signal (Summary and Column 5 line 10 – Column 6 line 50).

9. As to claim 2, Lotspiech teaches generating by the first device a first key signal using the control signal (Column 5 10 – 29); and

wherein encrypting or hashing comprises using the first key signal (Summary and Column 5 line 55 – Column 6 line 12).

10. As to claim 3, Lotspiech teaches generating by the second device a second key signal using the control signal (Column 6 lines 29 – 50);

and generating by the second device the destination parameter signal by decrypting or inversely hashing the encrypted parameter or hashed parameter signal using the second key signal (Summary and Column 6 line 29 – Column 6 line 50).

11. As to claim 4, Lotspiech teaches generating by the first device a key index signal (Column 5 lines 10 – 29);

generating by the first device a key variable signal (Column 5 lines 10 – 29);

transmitting by the first device to the second device the key index signal and the key variable signal (Column 5 lines 55 – Column 6 line 8);

receiving by the second device from the first device the key index signal and the key variable signal (Column 6 lines 3 – 8);

generating by the second device an intermediate key signal using the key index signal and a key table (Column 6 lines 29 – 50);

and generating by the second device the second key signal using the intermediate signal and the variable signal (Column 6 lines 29 – 50).

12. As to claims 8 and 9, Lotspiech teaches a key table module including indexed cryptographic keys, the key table module operably coupled to the control logic block, the key table module to generate an intermediate key signal using a key index signal received from the control logic block (Column 7 lines 25 – 51);

a key interface stage operably coupled to the key table module and the control logic block for generating a key signal using the intermediate key signal received from the key table module and key variable signal received from the control logic block (Column 7 lines 25 – 51);

and an inverse transformation module operably coupled to the key interface stage and the control logic block, the inverse transformation module to generate the destination parameter signal by decrypting or inversely hashing the encrypted or hashed parameter signal using the key signal received from the key interface stage (Column 6 lines 29 – 50 and Column 7 lines 25 – 51).

13. As to claim 14, Lotspiech teaches the first signal comprises a parameter signal and a portion of a control signal (Column 5 line 55 – Column 6 line 8);

the decrypted or inversely hashed signal comprises a decrypted or inversely hashed portion of the control signal and a decrypted or inversely hashed parameter signal (Column 6 lies 29 – 50); and

the second device stores the decrypted or inversely hashed parameter signal depending on a comparison of a portion of the control signal received from the first device and the decrypted or inversely hashed portion of the control signal (Column 6 lines 29 – 50 and Column 9 lines 6 – 18).

14. As to claim 15, Lotspiech teaches the decrypted or inversely hashed portion of the control signal comprises the key index (Column 5 lines 10 – 29).

15. As to claim 16, Lotspiech teaches transmitting, by the first device to the second device, a destination register signal (Column 5 lines 55 – 59 and Column 6 lines 8 – 32);

receiving, by the second device from the first device, the destination register signal (Column 6 lines 3 – 28);

storing, by the second device, at least a portion the decrypted or inversely hashed signal at a location determined in accordance with the destination register signal (Column 7 lines 4 – 51) .

Claim Objections

16. Claims 5 and 10 would be allowable if rewritten to overcome the rejection(s) under 35 U.S.C. 112, 2nd paragraph, set forth in this Office action and to include all of the limitations of the base claim and any intervening claims.

Conclusion

17. Examiner's Note: Examiner has cited particular columns and line numbers in the references as applied to the claims above for the convenience of the applicant. Although the specified citations are representative of the teachings in the art and are applied to the specific limitations within the individual claim, other passages and figures may apply as well. It is respectfully requested from the applicant, in preparing the responses, to fully consider the references in entirety as potentially teaching all or part of the claimed invention, as well as the context of the passage as taught by the prior art or disclosed by the examiner.

18. The prior art made of record and not relied upon is considered pertinent to applicant's disclosure. See PTO Form 892.

Applicant is urged to consider the references. However, the references should be evaluated by what they suggest to one versed in the art, rather than by their specific

disclosure. If applicants are aware of any better prior art than those are cited, they are required to bring the prior art to the attention of the examiner.

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Pramila Parthasarathy whose telephone number is 571-272-3866. The examiner can normally be reached on 8:00a.m. To 5:00p.m.. If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Ayaz Sheikh can be reached on 571-232-3795. Any inquiry of a general nature or relating to the status of this application or proceeding should be directed to the receptionist whose telephone number is 703-305-3900.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR only. For more information about the PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

Pramila Parthasarathy
October 27, 2005.

AYAZ SHEIKH
SUPERVISORY PATENT EXAMINER
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